Paper / Subject Code: 32323 / Linear Integrated Circuits

E(ELEX.) | SEM- V | LIC | R-19-Escheme | 09/06/25.

Time: 3 Hours

Max. Marks: 80

NB:	에 마이트를 하는데	
	remaining questions	
	2. Assume suitable data if necessary	
	3. Draw clean and neat diagrams	
		(20)
Q.1	Attempt the following questions.	(20)
a.	Draw block diagram of OPAMP and explain function of each block.	5
b.	Draw circuit diagram and frequency response characteristics of first order Low pass	
	filter and explain its operation?	5
c.	Explain why OPAMP is called linear IC?	5
d.	Compare ideal and practical Differentiator circuit?	5
Q.2.	a. Design a 2nd order KRC high Pass Filter with a cut-off frequency of 4 kHz and a	
	Quality Factor (Q) of 3.	10
2	b. Draw and explain the working of a RC PHASE SHIFT Oscillator using an Op-Am	p.
	State formula for frequency of oscillation.	10
2		
Q.3.	a. Explain with necessary diagrams and waveforms the principle of operation of a	
	Astable Multivibrator using IC 555.	10
	b. Design an IC 555 Astable Multivibrator for an output frequency of 1.2 kHz with a	
Dy.	duty cycle of 60%.	10
Mr.		
0.4		
Q.4.	a. Explain with a circuit diagram the principle of operation of a any one method	10
. 2	of ADC.	10
3	b. With a neat circuit, explain the working of the Weighted resistor type Digital to	10
3	Analog Converter (DAC).	10
05	a. Draw the circuit diagram and explain the operation of a Triangular Wave Gener	rator
2.0.	using Op-Amp. Also, describe the modifications required to obtain a Sawtooth W	
2	Output.	10
	b. Explain internal diagram of power amplifier LM 380.	10
	b. Explain internal diagram of power amplifier EN 300.	10
1/10		
Q.6	Write notes on following	20
	i) Peak detector	
	Ii) Voltage to current converter.	
1.		

O. P. Code: -86940

Page 1 of 1

Program Code:-