

15/05/2025 BE EXTC SEM-VIII C-SCHEME RF DESIGN QP CODE: 10083716

Time: 3 Hours

Marks: 80

Note:

1. Question No.1 is compulsory.
2. Attempt any three from the remaining questions.
3. Assume suitable data if applicable.
4. Figures on the right hand side indicate full marks.

Q1: Solve any four

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|---|----------|
| a) What is meant by electromagnetic compatibility? | 5 |
| b) Explain 1 dB compression in power amplifier designing | 5 |
| c) State and explain the term Unilateral Figure of Merit | 5 |
| d) State the conditions to generate oscillations for two port oscillator. | 5 |
| e) Explain multi point ground systems | 5 |

Q2: A) Design a composite low pass filter by image parameter method for the **10**

Following specifications

$$Z_0 = 50\Omega, f_c = 50 \text{ MHz}, f_{\infty} = 52 \text{ MHz}$$

B) Give the significance of each section in Image parameter method of filter **10**

Design.

Q3: A) A microwave transistor has the following S parameters at 10 GHz with 50Ω **10**

Source and reference impedance

$$S_{11} = 0.45 \angle 150^\circ, S_{12} = 0.01 \angle -10^\circ, S_{21} = 2.05 \angle 10^\circ, S_{22} = 0.40 \angle -150^\circ$$

The load impedance is 30Ω. Find out i) power gain ii) available power gain

B) Design a microwave amplifier using a GaAs FET to operate at 6 GHz with **10**

maximum transducer power gain. The transistor S parameters at a linear bias point

$$V_{ds} = 4V \text{ and } I_{ds} = 0.5 I_{dss} \text{ are}$$

$$S_{11} = 0.641 \angle -171.3^\circ, S_{12} = 0.057 \angle 16.3^\circ, S_{21} = 2.058 \angle 28.5^\circ, S_{22} = 0.572 \angle -95.7^\circ$$

Q4: A) List and explain the power amplifier performance parameters **10**

B) Compare various types of Diode and FET Mixers **10**

Q5: A) Write short note on CISPR and FCC standard **10**

B) Explain Kuroda's identity and Richard's Transformation. **10**

Q6: A) Explain Bonding and Shielding in EMC **10**

B) What is phase noise? How it is represented? **10**
