## T.E Electrical VI CBGS Mc 2 its apple

14.12.15

QP Code: 6426

				(3 Hours)	[ Total Marks:	80
	N.	B.: (1)	Question No. 1 is co	ompulsory.		1
		(2)		rom remaining questions.		
		(3)	Assume suitable data	The second secon		
		(4)	Figures to the right	t indicate full marks.		
1.	So	Solve Any four :-				
		(a) Wh	at is the significance	of STATUS register in PI	C 18F.	20
		(b) Cor	mpare microcontroller	r verses microprocessor		
			plain pipeline concept			
		(d) List	the general features of	f PIC 18 internally support	ted peripherals.	
		(e) Exp	olain Watch dog timer.		20	
0		*** *			100	
2.	(a)		nort note on assemble			10
7.54	(b)	(b) Explain parallel port structure of PIC 18F.				10
3.	(a)		addressing modes of I	A COLUMN TO A COLU		10
	(b)	Explain	memory organization	of PIC 18F.		10
158						
4.	(a)	Explain	what steps are taken b	y microcontroller when in	iterrupt occurs	10
	(I.V	and also	explain how interrupt	t service routine is execut	ed.	
	(0)	Explain	briefly the architectur	e of PIC microcontroller		10
5	(a)	Evaloin	difference and so	C DYCHOR	14/18	C.
5.	(a)	commun	ication registers	of PIC18F associated	with serial	10
	(b)		rotate and branch instr	metions of DICIPE		V 1521
	(-)	Lipidii .	Totale and branch mistr	detions of Pictor.		10
6.	(i)	A switch	(SW) is connected to	pin RD7 (PORTD.7). Wr	ita a program	10
	306	to monito	or the status of SW an	nd perform the following:	ite a program	10
		(a)	If SW=0, the stepper	motor moves clockwise.		
		(b)	If SW=1, the stepper	motor moves counter clo	ckwise.	
	(ii)	Assuming	g that XTAL = 10 MH	z, write a program to gene	erate a square	0
•	1	wave of	10ms on pin PORTB-3	3.		