## S.E. (ELECTRICAL) (SEM-III) (CBSGS) NOV, 2013 Electronic Devices and Circuits 25/11/2013

33 : 2nd half.13-Avi(ao)

Con. 7848-13.

GX-12031

		(3 Hours)	[Total Marks: 80	
N.B. :	(2)	Question No. 1 is compulsory.  Answer any three from remaining five questions.  Figures to the right indicate full marks.  Assume the data if it is necessary.		
	(4)	Assume the data in it is necessary.	10	
Q.1 Ans	swer	any four from the following	[20]	
a) Expla	ain th	ermal runway and stabilization		
b) Barki	hause	en criterion for sustained oscillation		
c) Zene	r dioc	e as a voltage regulator		
d) Expla	ain cri	terion for the selection of emitter by pass capacitor in an CE amplifie	er.	
e) FET	as dif	ferential amplifier		
Q.2 a) E	xplai	n how the amplification factor, input impedance, output impedance a	and bandwidth are	
		negative feedback	[10]	
		ne operation of C type filter in full wave rectifier with all nece	essary diagrams and	
wavefor			[10]	
2.3 a) E	Explai	n UJT as a relaxation oscillator in detail. Find frequency of oscillator	[10]	
) Wha	t is N	OSFET? Explain the construction and characteristics of N - change	nel MOSFET with the	
		le diagram.	[10]	
Q.4 a) D	Derive	the expression for voltage gain, current gain, input impedance and	output impedance of	
CB amplifier.			[10]	
b) Explain the AC Analysis of Dual Input Unbalance Output Differential Amplifier.			[10]	
0.5 a) E	xolai	n the working of CS FET amplifier.	[10]	
		equation for frequency of oscillation of Colpitts oscillator. Also de	erive the condition for	
		cillation.	[10]	
Q.6 a) V	Vhat	is Darlington pair? What are its features? How to bias the pair? De	rive expression for its	
c parar			[10]	
	100	differences between LC and RC oscillators. Give examples of ea	ach of oscillators with	
requencies.				