F.4. B. St. Comp. Sci. Sem. I Nov. 2017

COD

S0131 / S9052 / COMPUTER ORGANIZATION & DESIGN.

Q.P.Code: 12186

(2½ H	ours)	[Total M	arks: 75
N.B.	1) All	3.5	
IN.D.	1) All questions are compulsory.		
	2) Figures to the right indicate marks.		1
	3) Illustrations, in-depth answers and diagrams will be appreciated.		1 1
	4) Mixing of sub-questions is not allowed.	1	
Q. 1	Attempt All(Each of 5Marks)		(15)
(a)	Select appropriate choice from the following:		
	i. ASCII code is bit code.	0	
	a) 2 b) 5 c) 16 d) 8		e 1,0
	ii. Which of the following system is digital.		
		Mercury	
	Thermometer d) None of the above		3
	iii. If one of the input to an OR gate is high its output will be	× .x .x.	
	a) Medium b) High c) Low		
	iv. The assembled machine language program is called		
	a) Object Code 'b)Executable code c) Source code		
	v. The number of data registers in coldfire processor is		
,	a) 2 b) 4 c) 8 d) None of these		
(b)	Till in the blanks		
(b)	Fill in the blanks. 1. If one of the inputs to an OR gate is high its output will be		
	2. The number of inputs to a logic gate is right to dutput will be		
	3. In decimal number system base is		
	4. A K-map of n variables contains cells.		
	5. CISC stands for		
(c)	Short Answers.		
(6)	i. Define Sequential circuit.		X
3 45 8	ii. What is the binary equivalent of decimal 25?		
	iii. 'What is parity bit?		
	iv. Define fan-out.		
4	v. Define exception.		
Q. 2	Afternot the following (Amy THEET)/F. J. CEAR 1.		
(a)	Attempt the following (Any THREE) (Each of 5Marks)		(15)
(b)	Draw a neat basic block diagram of computer system.		
(c)	State& explain number systems used in computer system. What is the role of shift register? Explain with 4 bit shift register.		
(d)	What is the role of shift register? Explain with 4-bit shift register. What is gated S-R latch?		
(e)	Explain tristate buffers.		
(f)			
(-)	Explain the concept of universal gate.	*	

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Q. 3	Attempt the following (Any THREE) (Each of 5Marks)	(15)	
(a)	Define terms: Memory word, word length, Address & address space.	. 4	
(b)	Explain How memory is used in read write operations.		
(c)	The HLL statement z=x*y when gets compiled what type of machine instructions will get used?		
(d)	Explain characteristics of CISC instruction set.		
(e)	What is pointer? Explain its use in indirection operation.	1)5	
(l)	Discuss the typeof machine instructions.		
Q. 4	Attempt the following (Any THREE) (Each of 5Marks)	(15)	
(a)	Explain arithmetic, logic & Load instructions with example.	(10)	
Value (1900)		35	
(b)	Discuss the conceptual view required for computing.		
(c)	How data movement & manipulation operations performed using Data Path.	20	
(d)	With neat diagram explain organisation of instruction fetch section of the processor.		
(e)	What is an exception? Give example.		
(f)	Explain program controlled I/O.		
Q. 5	Attempt the following (Any THREE) (Each of 5Marks)	(15)	
(a)	Explain implementation of AND, NOT GATES using NOR.		
(b)	Explain the use of Stacks in computer operations with example.		
(c)	What are the components of processor?		
(d)	Convert decimal number 356 to binary & octal form.		
(e)	Explain instruction execution & straight line dsequencing.	*	