

(L)

T-E - Boomed (Rev) V

Micro pro

19/5/15

(14)

QP Code : 3348

(3 Hours)

[Total Marks : 80

- N.B. :
- (1) Question no.1 is compulsory.
 - (2) Attempt any three from the remaining questions.
 - (3) Assume suitable data wherever necessary.

1. (a) Explain segmentation in 8086. 20
(b) Draw and explain general purpose computer architecture.
(c) Explain 8288 bus controller.
(d) Compare memory mopped I/O and I/O mapped I/O.
(e) Enlist important features of 8087.
2. (a) Draw and explain working of 8086 in maximum mode. Explain the read cycle of 8086. 10
(b) Explain with examples the various addressing modes of 8086. 10
3. (a) Write a programme to compute resonant frequency using 8087. Assume suitable values of L&C. 10
(b) Draw the interfacing diagram of 8259 with 8086 and explain the operation. 10
4. (a) Explain the various working modes of 8255. 10
(b) Draw and explain control register and status register of 8087. 10
5. (a) Write a program to multiply two 16 bit hexadecimal nos and store the result in memory location. 10
(b) Explain the interrupt structure of 8086. 10
6. Write short notes on the following. 20
 - (a) Push and POP instructions of 8086
 - (b) 8086 interfacing with 7- segment display.
 - (c) Compare Harvard and Von-Neumann CPU architecture
 - (d) Generation of 20 bit physical address using suitable example.

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