

S.E - IV Sem - Biomed
Electronic Circuits Analysis &

(1)

Design - II

04/12/2015

(19)

SE/IV/CBGS/BM/ECAD-II

QP Code : 5407

(3 Hours)

[Total Marks :80

- N.B. : (1) Question no. 1 is compulsory.
(2) Attempt any three questions from the remaining five questions.
(3) Assume suitable data if required.

1. (a) With definitions, ideal values and practical values explain CMRR and slew rate of op-amp 741 5
(b) Compare class A and class B power amplifier 5
(c) Explain current mirror circuit used in differential amplifier 5
(d) Give characteristics of negative feedback 5
2. (a) Design class A transformer coupled power amplifier to obtain 6w output to 10Ω load. 10
(b) With neat diagram derive voltage gain formula for 3 op-amp instrumentation amplifier Explain its characteristics. 10
3. (a) Derive frequency of oscillations and condition for sustained oscillations for wein bridge oscillator along with ckt diagram. 10
(b) For the following given specifications for DIBO differential amplifier 10
 $R_C = 3.3k, V_{BE} = 0.7V,$
 $R_{in1} = R_{in2} = 100\Omega, R_E = 1k,$
 $V_{CC} = V_{EE} = |20V|, \beta_{ac} = \beta_{dc} = 100$
Calculate $I_C, V_{CEQ}, A_d, A_c, CMRR, R_i, R_o.$ 5
4. (a) Design RC phase shift oscillator for $f = 1KHz$ 5
(b) Design op-amp based ckt for $V_0 = -(2V_1 + 4V_2 + 6V_3)$ 5
(c) Design a circuit using op-amp.
$$V_o = -\int_0^t V_{in} dt$$
 5
(d) Write design steps for heat sink
5. (a) Explain the advantages of negative feedback. Compare any two types of negative feedback circuits deriving its components. 10
(b) Derive relations of dc and ac analysis of Dual Input Unbalanced Output differential amplifier. 10
6. (a) Write short notes on the following (any two):- 20
(i) Precision Rectifier (Half wave and Full wave rectifier)
(ii) Gyrator
(iii) Log and antilog amplifier

[TURN OVER

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DATA SHEET

Transistor type	P _{dmax} @ 25°C Watts	I _{cm} @ 25°C Amps	V _{cc} V _{cc} volts d.c.	V _{ce} volts d.c.	V _{ce} (Sus) volts d.c.	V _{ce} (Sus) volts d.c.	V _{ce} (Sus) volts d.c.	V _{ce} (Sus) volts d.c.	V _{ce} (Sus) volts d.c.	V _{ce} (Sus) volts d.c.	V _{ce} (Sus) volts d.c.	T _j °C	D.C. current		Small signal r _{in} Ω	I _μ max.	V _{az} max. V	θ _{jc} °C/W	Derate above 25°C W/°C
													min	typ.					
2N 3055	115.5	15.0	1-1	100	60	70	90	7	200	20	50	70	15	50	120	1.8	1.5	0-7	
ECN 055	50.0	5.0	1-0	60	50	55	60	5	200	25	50	100	25	75	125	1.5	3.5	0-4	
ECN 149	30.0	4.0	1-0	50	40	—	—	8	150	30	50	110	33	60	115	1.2	4.0	0-3	
ECN 100	5.0	0.7	0-6	70	60	65	—	6	200	50	90	280	50	90	280	0.9	35	0-05	
BC147A	0.25	0.1	0-25	50	45	50	—	6	125	115	180	220	125	220	260	0.9	—	—	
2N 525(FNP)	0.225	0.5	0-25	85	30	—	—	—	110	35	—	65	—	45	—	—	—	—	
BC147E	0.25	0.1	0-25	50	45	50	—	6	175	200	290	450	240	330	500	0.9	—	—	

BFV 11—JFET MUTUAL CHARACTERISTICS

Transistor type	h _{ie}	h _{oc}	h _{re}	θ _{ja}	-V _{gs} volts				I _{os} max. mA				I _{os} typ. mA				I _{os} min. mA			
					0.0	0.2	0.4	0.6	0.8	1.0	1.2	1.6	2.0	2.4	2.5	3.0	3.1	3.3	3.5	4.0
BC 147A	2.7 K Ω	10 μ Ω	1.5 × 10 ⁻⁴	0-4°C/mw	0.0	0.2	0.4	0.6	0.8	1.0	1.2	1.6	2.0	2.4	2.5	3.0	3.5	4.0		
2N 525 (PNP)	1.4 K Ω	25 μ Ω	3.2 × 10 ⁻⁴	—	10	9.0	8.3	7.6	6.8	6.1	5.4	4.2	3.1	2.2	2.0	1.1	0.5	0.0		
BC 147B	4.5 K Ω	30 μ Ω	2 × 10 ⁻⁴	0-4°C/mw	7.0	6.0	5.4	4.6	4.0	3.3	2.7	1.7	0.8	0.2	0.0	0.0	0.0	0.0		
ECN 100	500 Ω	—	—	—	4.0	3.0	2.2	1.6	1.0	0.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0		
ECN 149	250 Ω	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—		
ECN 055	100 Ω	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—		
2N 3055	25 Ω	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—		

N-Channel JFET

Type	V _{gs} max. Volts	V _{gs} max. Volts	P _d max. @ 25°C	T _j max.	I _{os}	r _{in} (typical)	-V _p Volts	r _s	Derate above 25°C	θ _{jc}
2N3822	50	50	300 mW	175°C	2 mA	3000 μ Ω	6	50 K Ω	2 mW/°C	0.59°C/mW
BFV 12 (typical)	30	30	300 mW	200°C	7 mA	5600 μ Ω	2.5	50 K Ω	—	0.59°C/mW

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