

Electronic Circuits and Design - I

18-11-2013-DTP-P-7-RA-6

Con. 8961-13.

(17)

GX - 12158

(3 Hours)

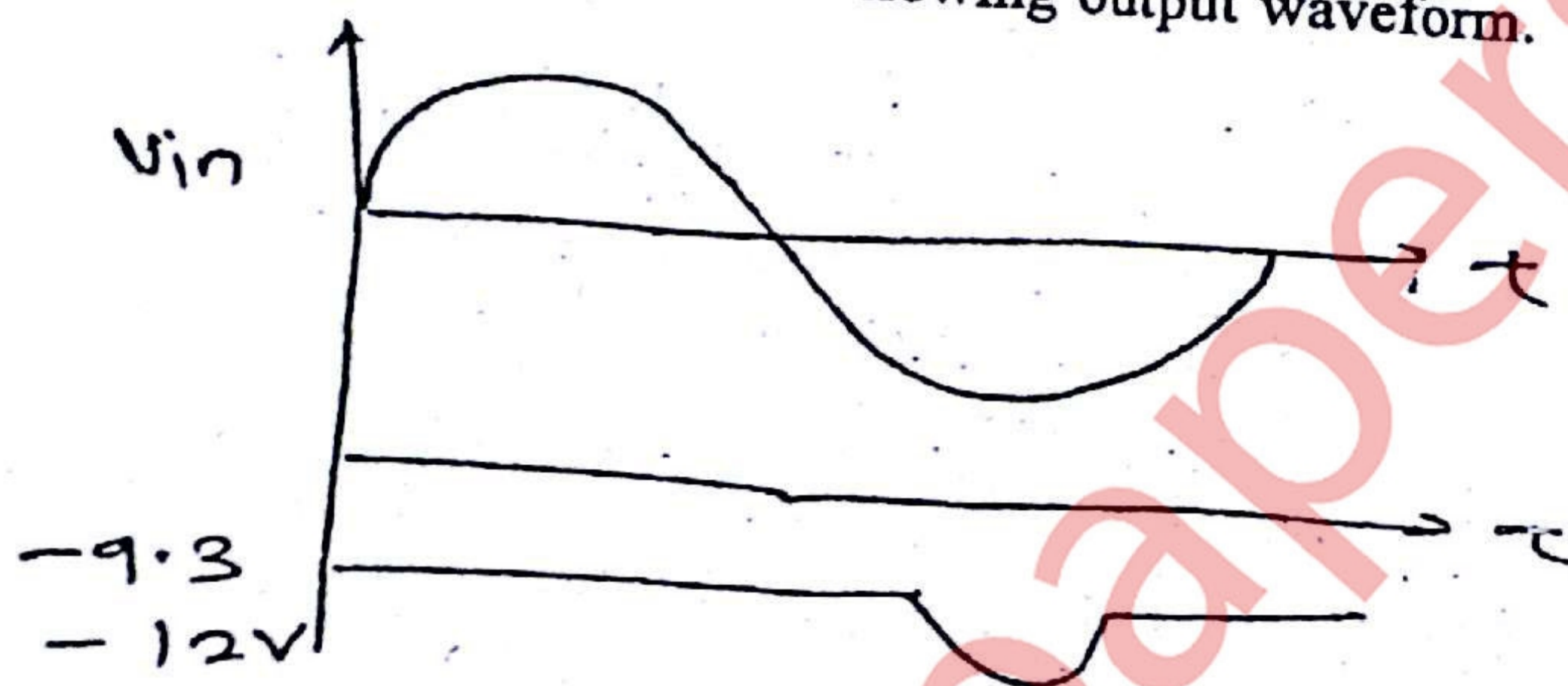
[Total Marks : 80

- N. B. :
- (1) Question No. 1 is compulsory.
 - (2) Attempt any three out of remaining five questions.
 - (3) Assume suitable data wherever necessary.

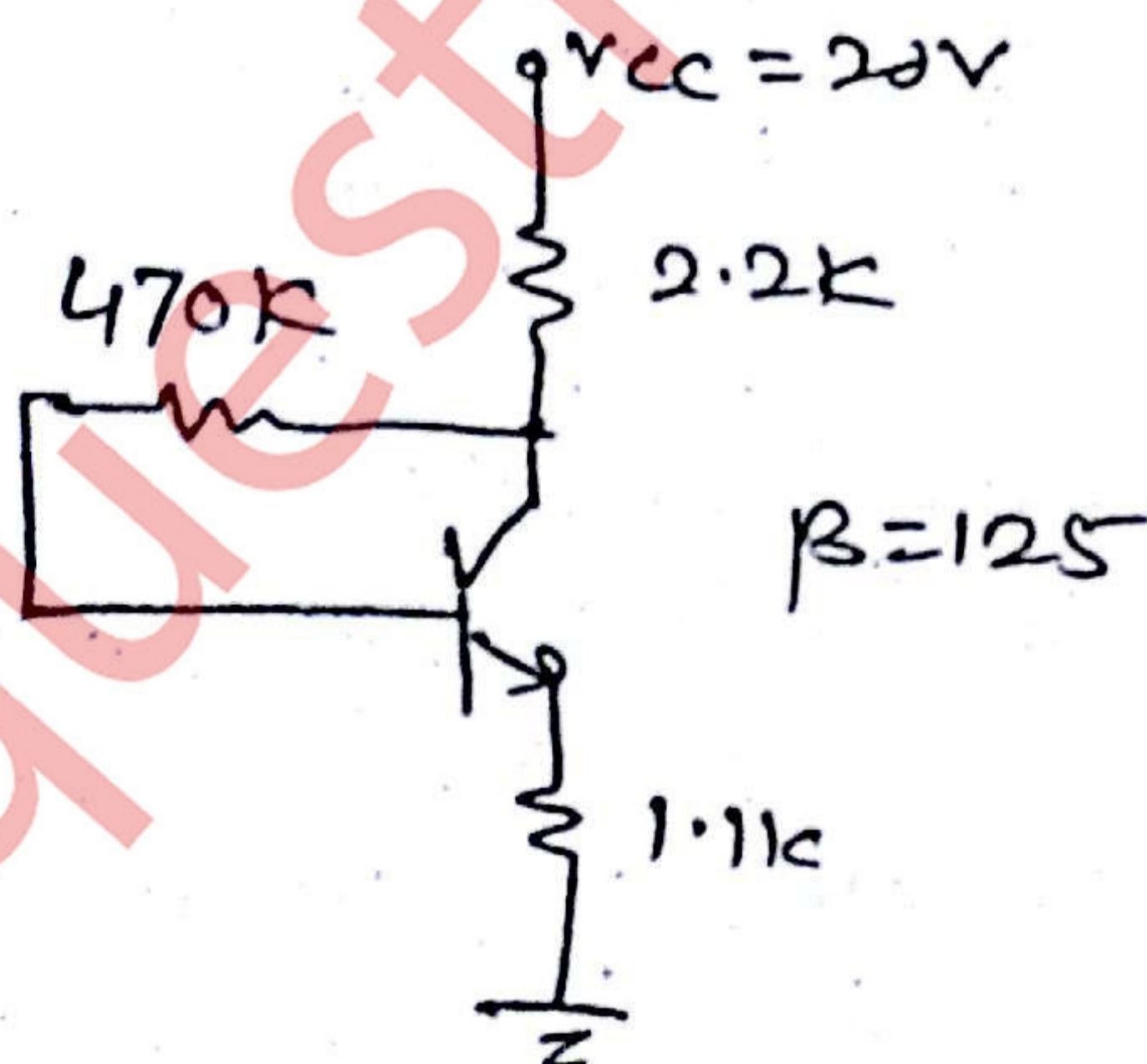
1. Attempt any four of following : -

- (a) Explain Avalanche and Zener breakdown
- (b) What is zero temperature drift?
- (c) Discuss working of enhancement MOSFET with characteristics and equation.
- (d) Design a clipper to give the following output waveform.

20



- (e) Calculate for the following circuit.
- (i) I_{B1}
 - (ii) I_C
 - (iii) V_{CE}
 - (iv) BJT power consumption.

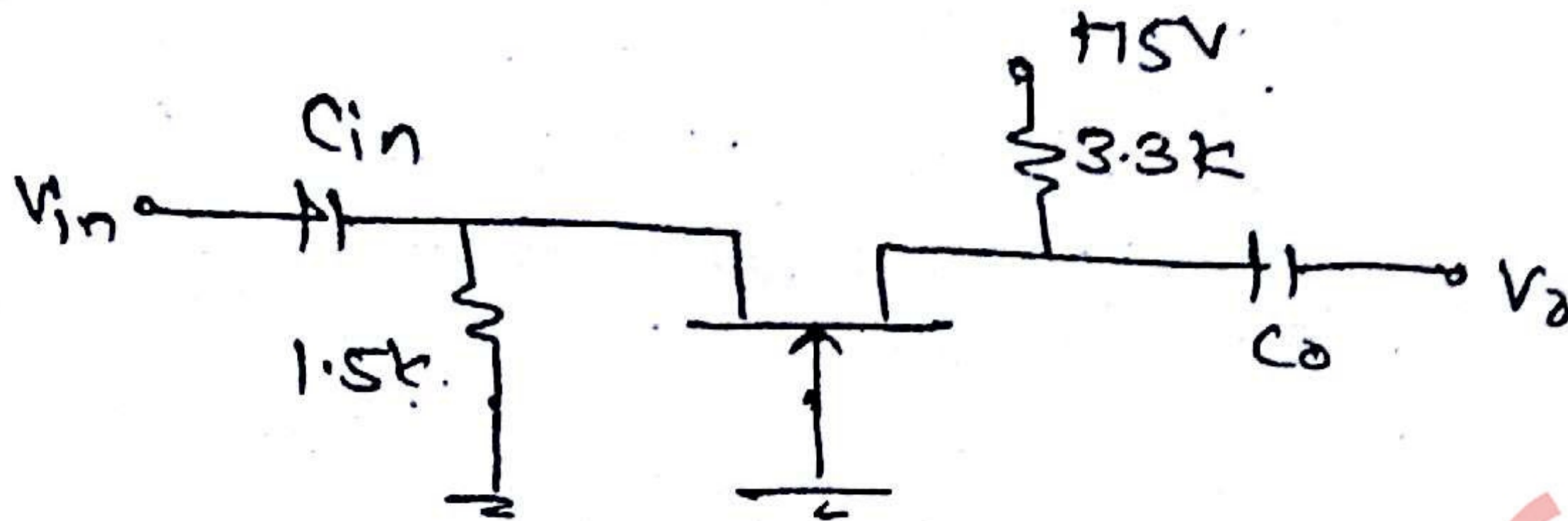


2. (a) Draw various biasing schemes of BJT and calculate their stability factor.

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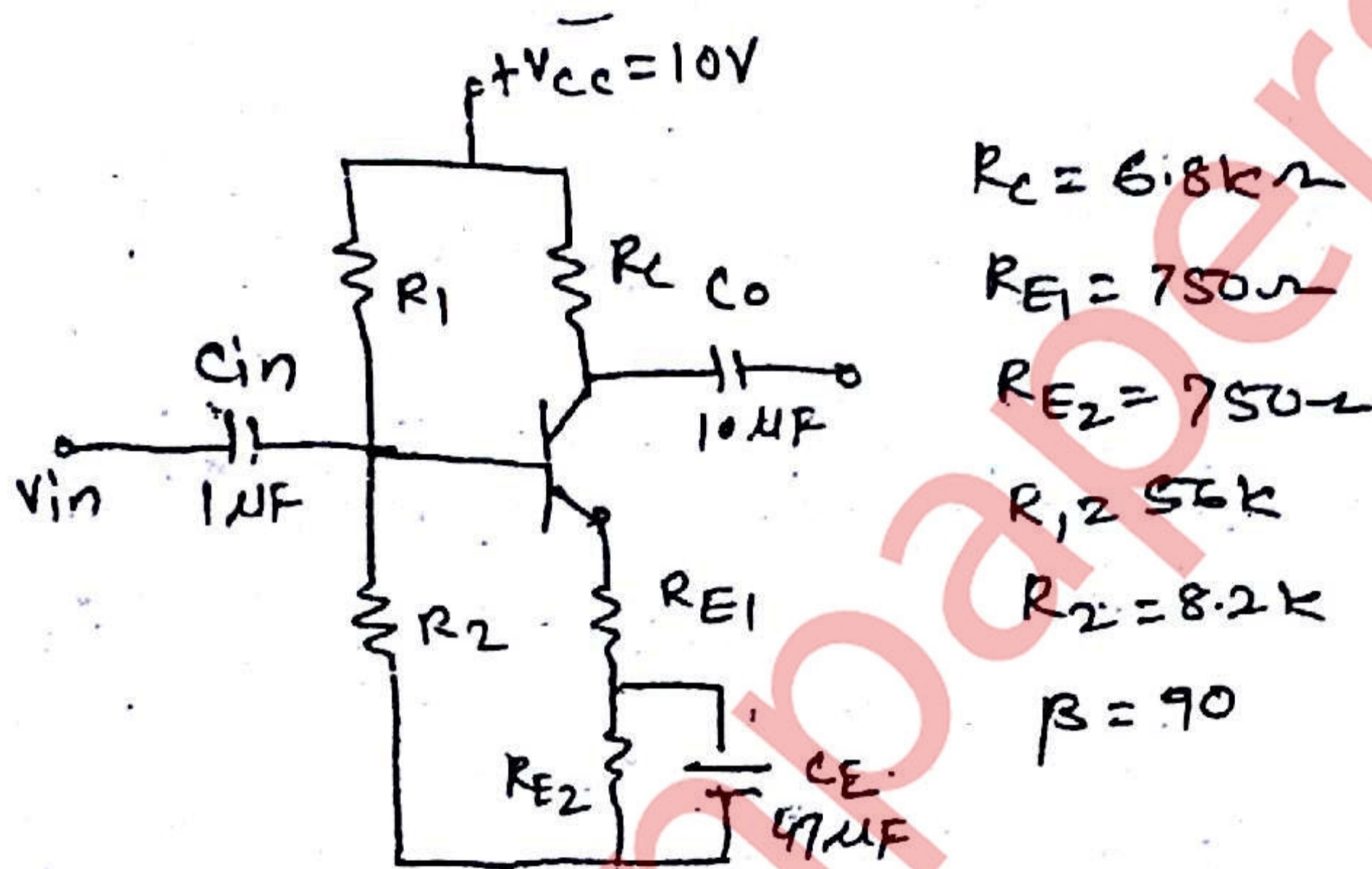
(b)



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Determine Z_{in} , Z_o and V_o , if $V_{in} = 0.1 \text{ mV}$, $r_d = 40 \text{ k}\Omega$.

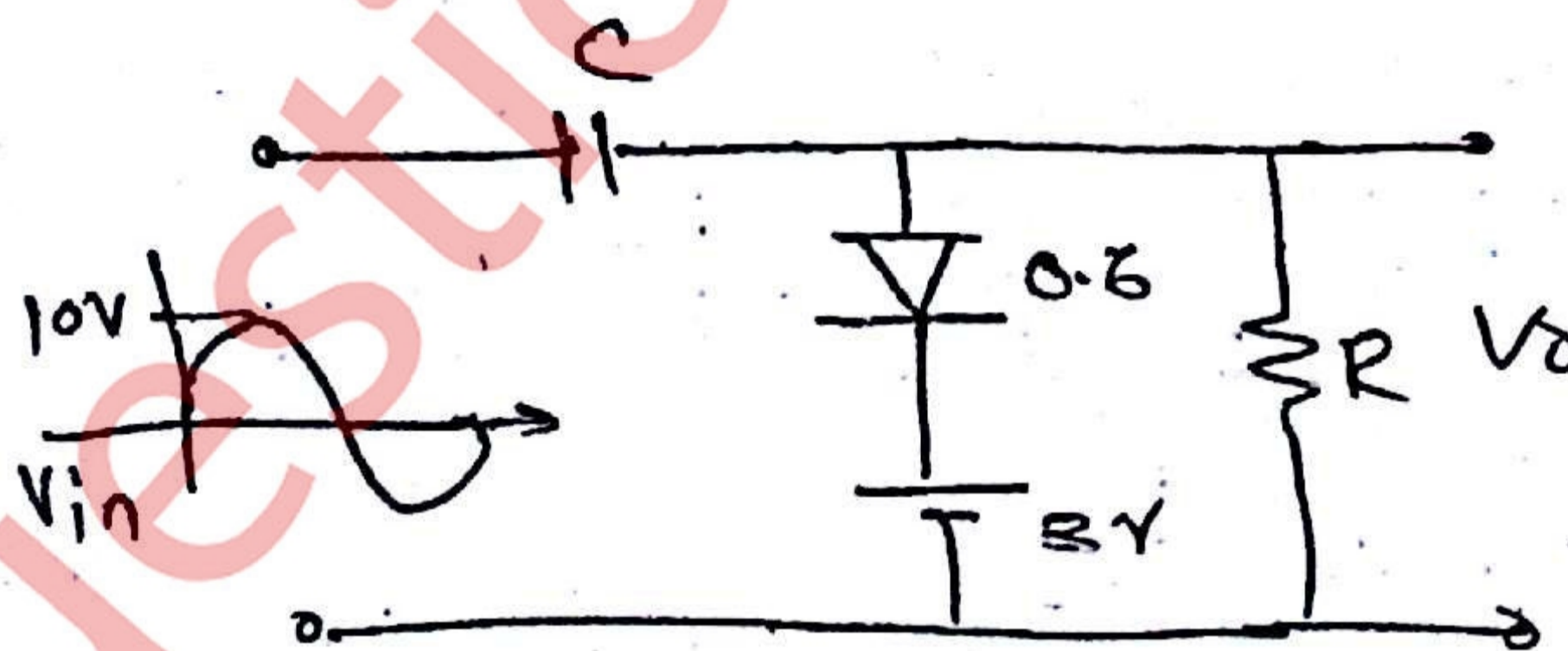
3. (a)



10

Find Q-point and calculate A_v , Z_i , Z_o

(b)



5

Explain with neat output waveform working of given circuit.

(c) Explain Thermal Runaway in detail.

5

4. (a) Design a single state RC coupled CE Amplifier to meet following specifications. 20

Use BC 147 B.

$$|A_v| \geq 240$$

$$S \leq 10$$

$$f_L = 20 \text{ Hz}$$

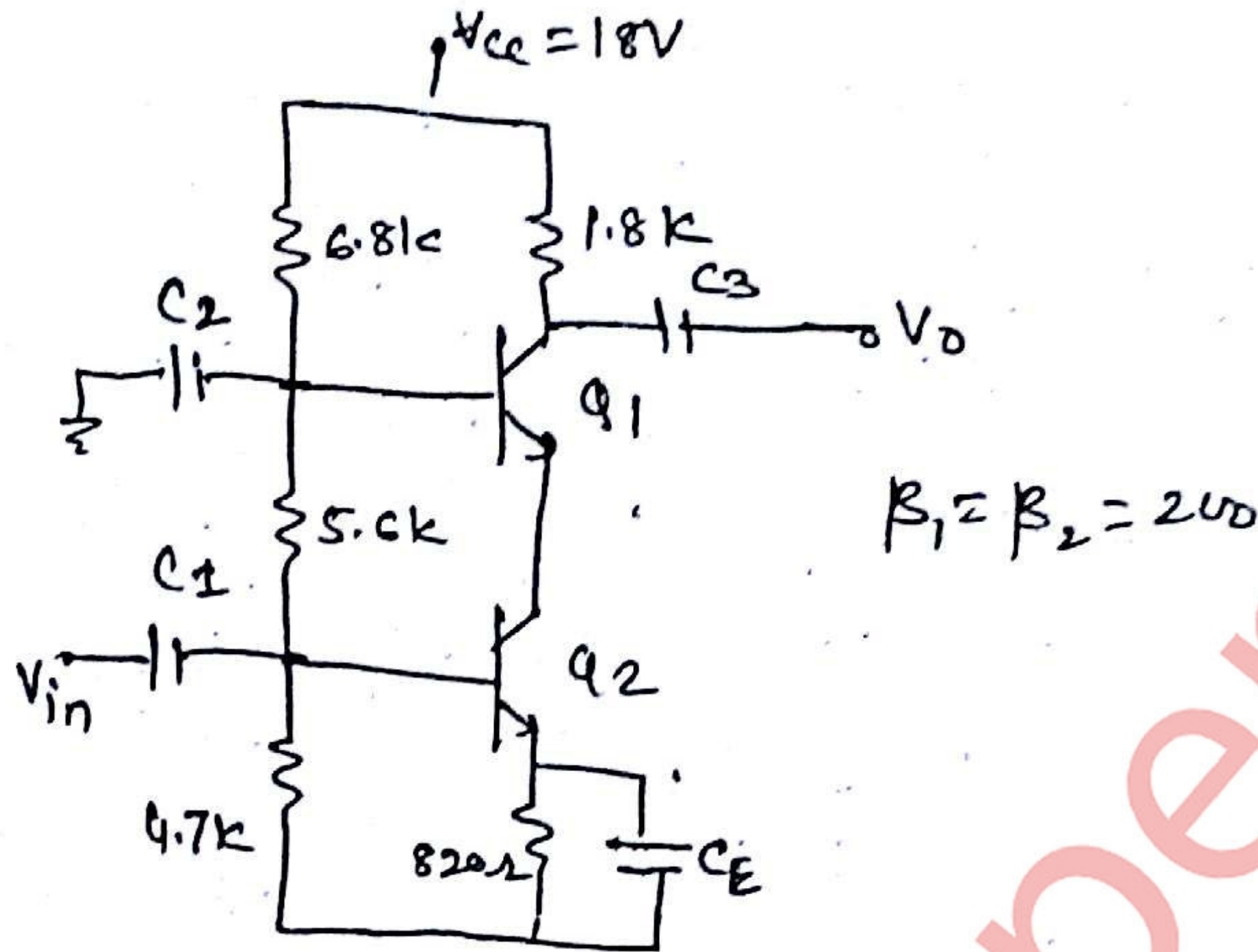
$$V_o = 2 \text{ V}$$

For the above designed Amplifier calculate A_v , Z_i , Z_o .

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5. (a) Determine A_v , A_i and Z_{in} for the following circuit.

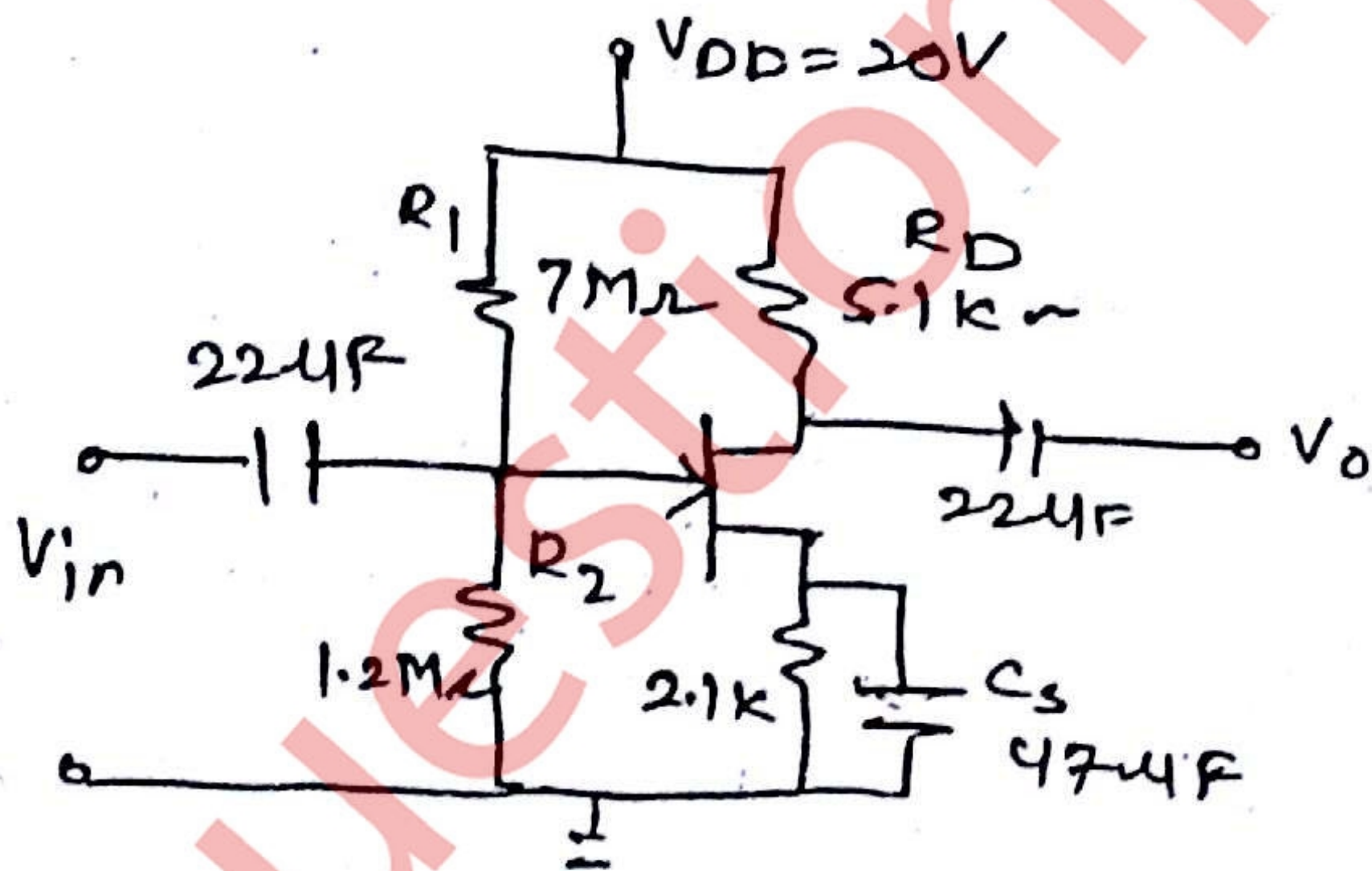
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(b) Draw a neat diagram of JFET CG Amplifier. Derive expression for A_v , Z_i , Z_o . 10

6. (a) Draw a neat diagram of BJT CE Amplifier with RE unbypassed and derive expression for A_v , Z_i , Z_o , A_i . 10

(b) 10



$I_{DSS} = 10\text{mA}$, $V_p = -3.5\text{V}$

For the above circuit calculate :

- (i) V_{DSQ}
- (ii) A_{v1}
- (iii) R_i , R_o
- (iv) F_L (Lower cut off freq.)